

Notice of Allowability

Application No.

09/751,802

Examiner

Thomas H. Stevens

Applicant(s)

ROESNER ET AL.

Art Unit

2123

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 9/30/05.
2. ☒ The allowed claim(s) is/are 1, 2, 5, 6, 8-12, 14-16.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |

DETAILED ACTION

1. Claims 1,2,5-6, 8-12, 14-16 were examined and allowed.
2. Claims 3,4,7, and 13 were cancelled.

Section I: Response to Applicants' Arguments (Appeal Brief)

3. Appellants' arguments set forth within the Appeal brief filed on 9/30/05, were acknowledged and persuasive to forego an examiner's answer, thus the rejections are no longer maintained and are withdrawn.

Furthermore, in view of the grounds of rejection traversed by applicants, the case is in condition for allowance.

Section II: Allowance

4. The following is an examiner's statements for reasons for allowance.

While Bargh et al. (US Patent 6,202,042) discloses a computer-readable medium stored thereon a data structure for use in a computer-aided design and verification system for naming simulation events tracked by instrumentation logic within a simulation model of a compiled digital circuit design that includes one or more design entities described utilizing a hardware description language, comprising: an eventname field containing data representing a simulation event; an instrumentation entity field containing data representing an instrumentation entity that generates said simulation event; a design entity field containing data representing an instrumentation entity that generates said simulation event; a design entity field containing data representing an

entity name of a design entity; and an instantiation identifier field containing data specifying a hierarchical instance of said design entity in which said simulation event is generated by said instrumentation entity (claim 1); Bargh et al. (US Patent 6,223,142) discloses a method for naming and processing a simulation event tracked by instrumentation logic within a simulation model of a compiled digital circuit design that includes one or more design entities described utilizing a hardware description language, said method comprising: associating an eventname, an instrumentation entity identifier, a design entity identifier, and an instantiation identifier with a simulation event, wherein said eventname represents a name of said simulation event, said instrumentation entity identifier represents an instrumentation entity that generates said simulation event, said design entity identifier is a design entity name specifying a design entity, and said instantiation identifier specifies a hierarchal instance of said design entity in which simulation event is generated by said instrumentation entity; and evaluating occurrences of said simulation event within said simulation model (claim 10). None of these references taken either alone or in combination with the prior art of record disclose an extended event identifier, including:

“an extended event identifier data structure”

in combination with the remaining elements and features of the claimed invention. It is for these reasons that the applicants' invention defines over the prior art of record.

Examiner's Amendment

5. An examiner's amendment to the record appears below.

The application has been amended as follows:

5a. Abstract, line 6, after the phrase "statement includes" -- an extended event identifier data structure -- has been inserted.

5b. Specification pg. 43, line 2, unbold number "**105**" to --105 -- has been inserted.

5c. Specification pg. 43, line 3, unbold number "**25**" to -- 25 -- has been inserted.

5d. Specification pg. 43, line 15, unbold number "**40**" to -- 40 -- has been inserted.

5e. Specification pg. 43, line 15, unbold number "**55**" to -- 55 -- has been inserted.

5f. Specification pg. 44, line 6, unbold "**105**" to --105 -- has been inserted.

5g. Specification pg. 44, line 6, unbold "**110**" to --110 -- has been inserted.

5f. Page 1 of the specification, the paragraph reciting "The present application is related to..." has been replaced by the following:

-- The present application is related to the following co-pending U.S. Patent Applications: U.S. Patent Application Serial No. 09/345,163 (Docket No. AT9-98-726) filed on 06/29/99, now U.S. patent No. 6,470,478 titled "Method And System For Counting Events Within A Simulation Model"; U.S. Patent Application Serial No. 09/729,465 (Docket No. AUS920000222US1) filed on December 5, 2000, now U.S. patent No. 6,978,231 titled "Embedded Hardware Description Language Instrumentation"; U.S. Patent Application Serial No. 09/75,252 (Docket No.

Art Unit: 2123

AUS920000225US1) filed on December 30, 2000, now U.S. patent No. 6,920,418 titled "Detecting Events Within Simulation Models"; U.S. Patent Application Serial No. 09/752,254 (Docket No. AUS920000228US1) filed on December 30, 2000, now U.S. patent No. 6,941,257 titled "Hierarchical Processing Of Simulation Model Events"; U.S. Patent Application Serial No. 09/751,803 (Docket No. AUS920000227US 1) filed on December 29, 2000, titled "Signal Override For Simulation Models"; and U.S. Patent Application Serial No. 09/752,251 (Docket No. AUS920000226US1) filed on December 30, 2000, titled "Randomized Simulation Mode Instrumentation". The above-mentioned patent applications are assigned to the assignee of the present invention. --

Should changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312 . To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Correspondence Information

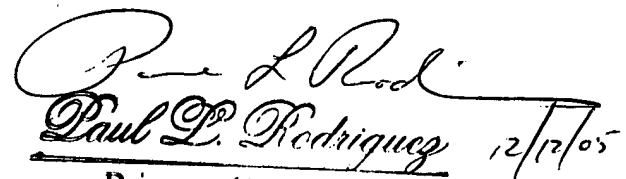
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mr. Tom Stevens whose telephone number is 571-272-3715, Monday-Friday (8:00 am- 4:30 pm EST).

If attempts to reach the examiner by telephone are unsuccessful, please contact examiner's supervisor Mr. Leo Picard ((571) 272-3749). The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2123

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>.. Answers to questions regarding access to the Private PAIR system, contact the Electronic Business Center (EBC) (toll-free (866-217-9197)).

December 5, 2005


Paul L. Rodriguez 12/12/05
Primary Examiner
Art Unit 2125

TS